ABSTRACT OF THE DISCLOSURE

The invention provides a data processing device with which disadvantages due to instruction allocation and decreased memory when adding a condition specifying field for executing conditional instructions can be prevented, and in which pipeline hazards due to the non–fulfillment of conditions can be suppressed when instructions with contradicting conditions are performed successively. The data processing device reads in and executes instructions in a certain sequence, and includes a fetch portion for reading in a computational instruction, a decoding portion for decoding the computational instruction that has been read in, an execution portion for executing the decoded computational instruction, and an instruction overriding control circuit that overrides, of one or a plurality of subsequent instructions that follow the computational instruction in said sequence and are associated with the computational instruction, all but one of the subsequent instructions, in accordance with an execution result of the computational instruction.